

Fig.1 (Prior Art)

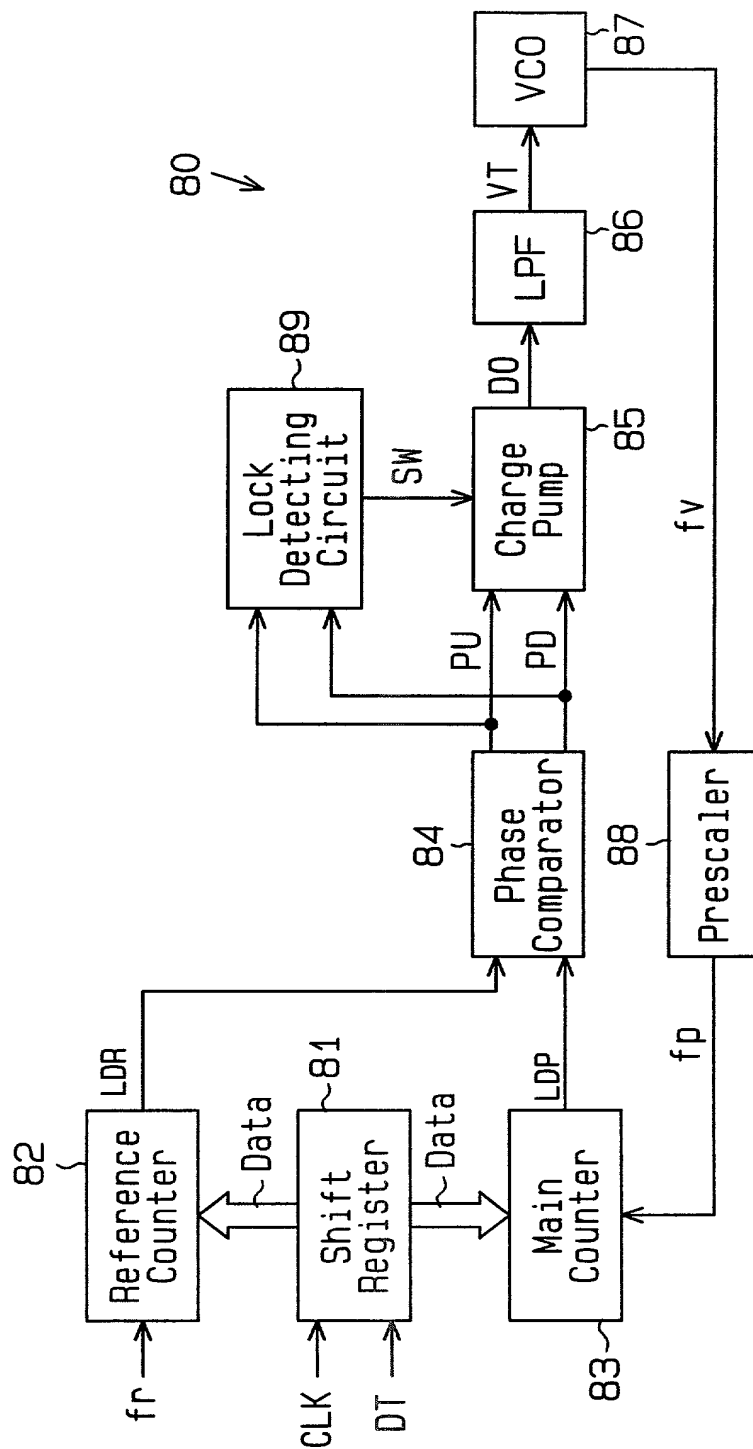


Fig. 2

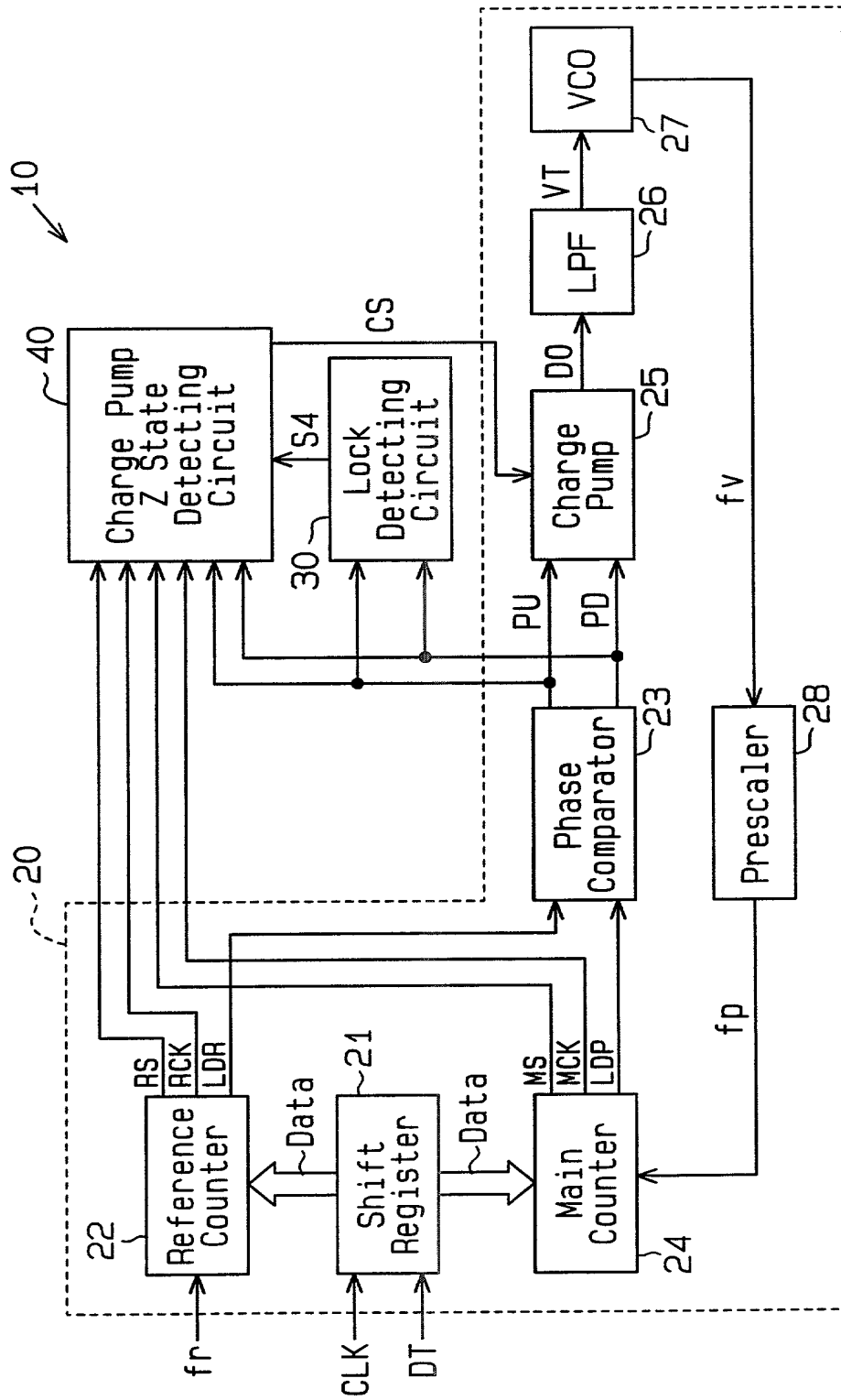


Fig. 3

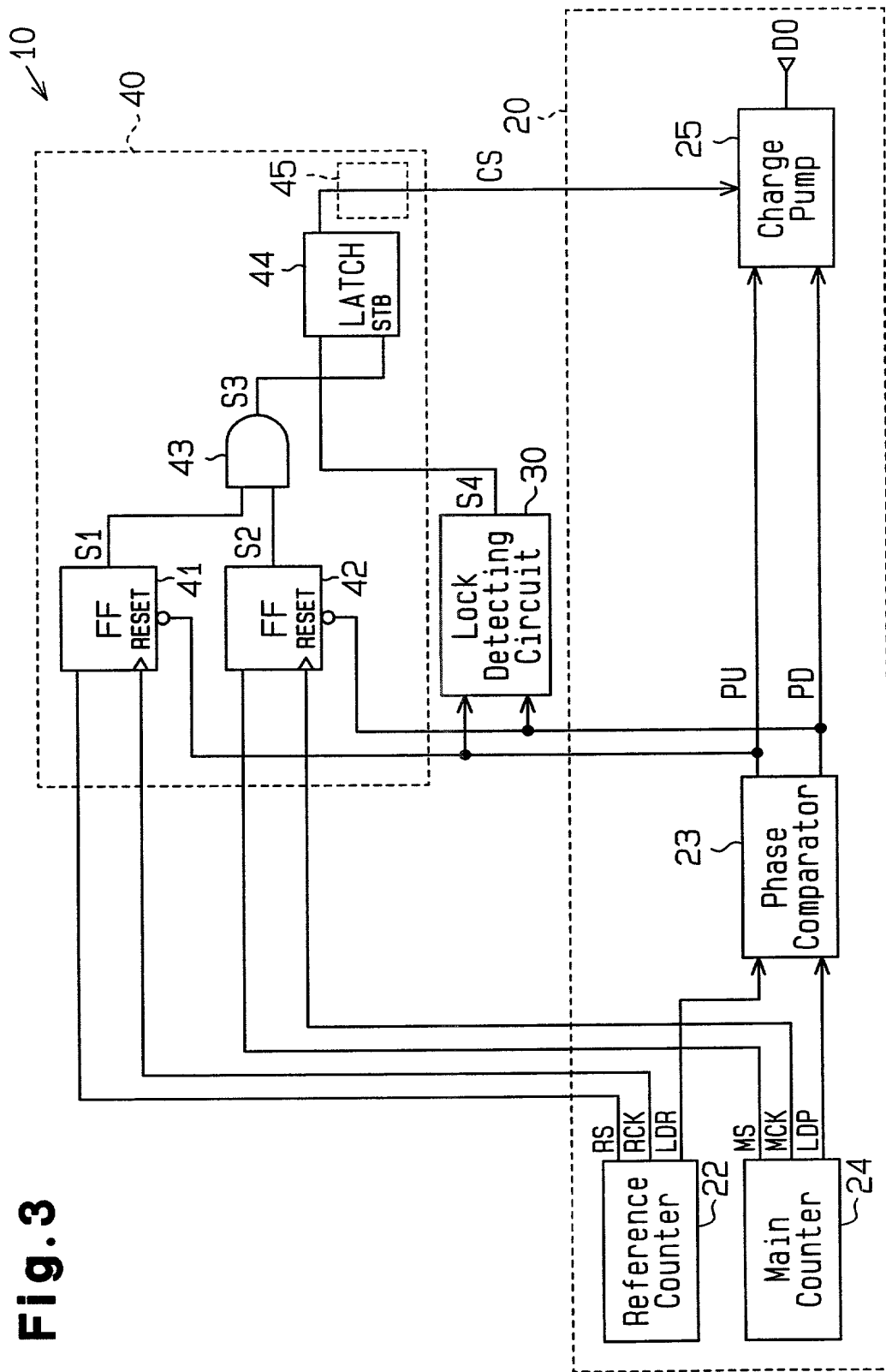


Fig. 4A

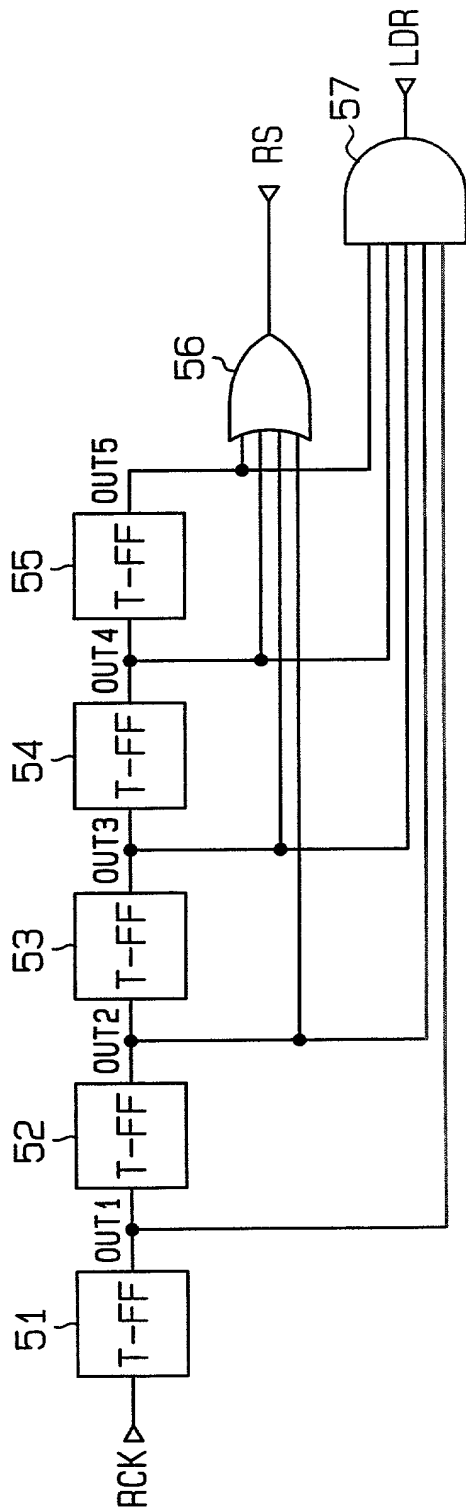


Fig. 4B

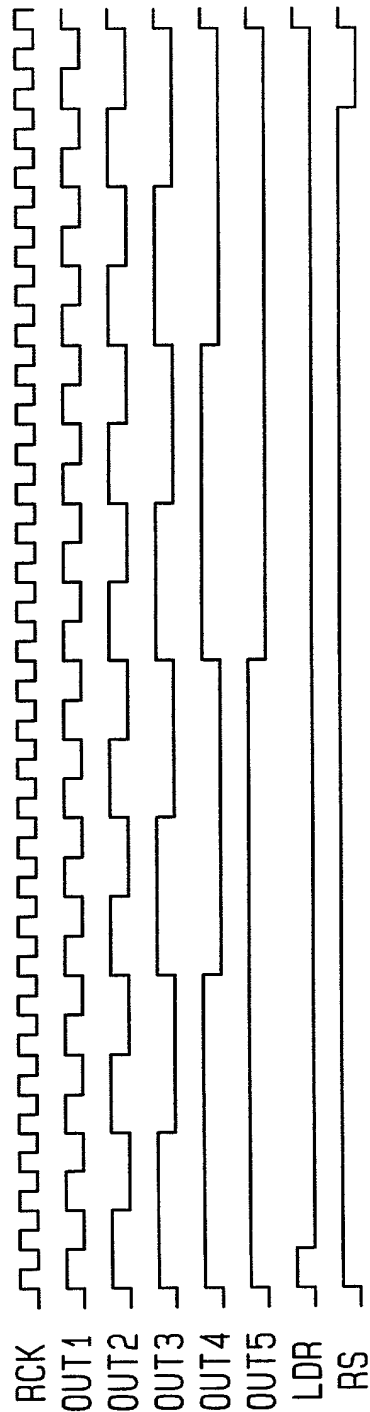


Fig.5

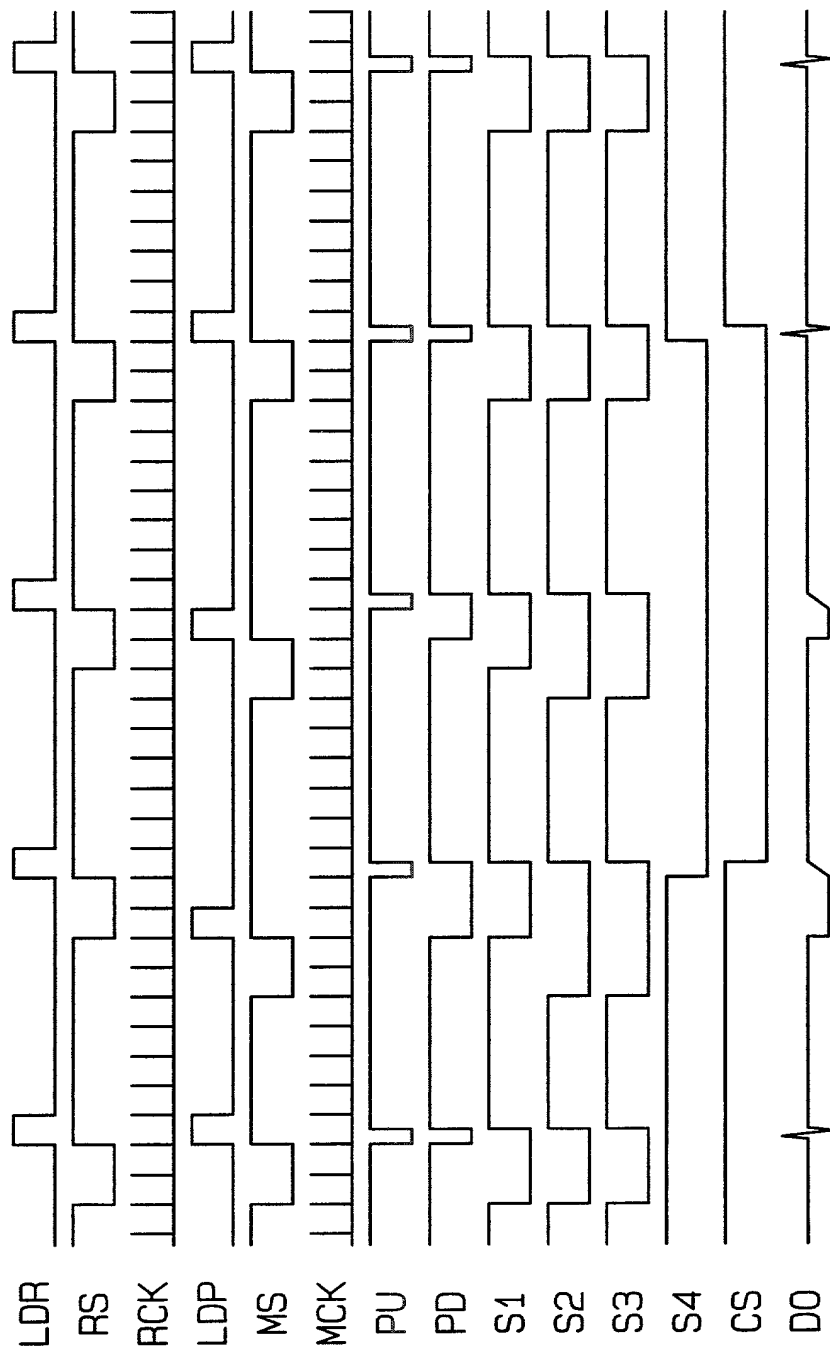
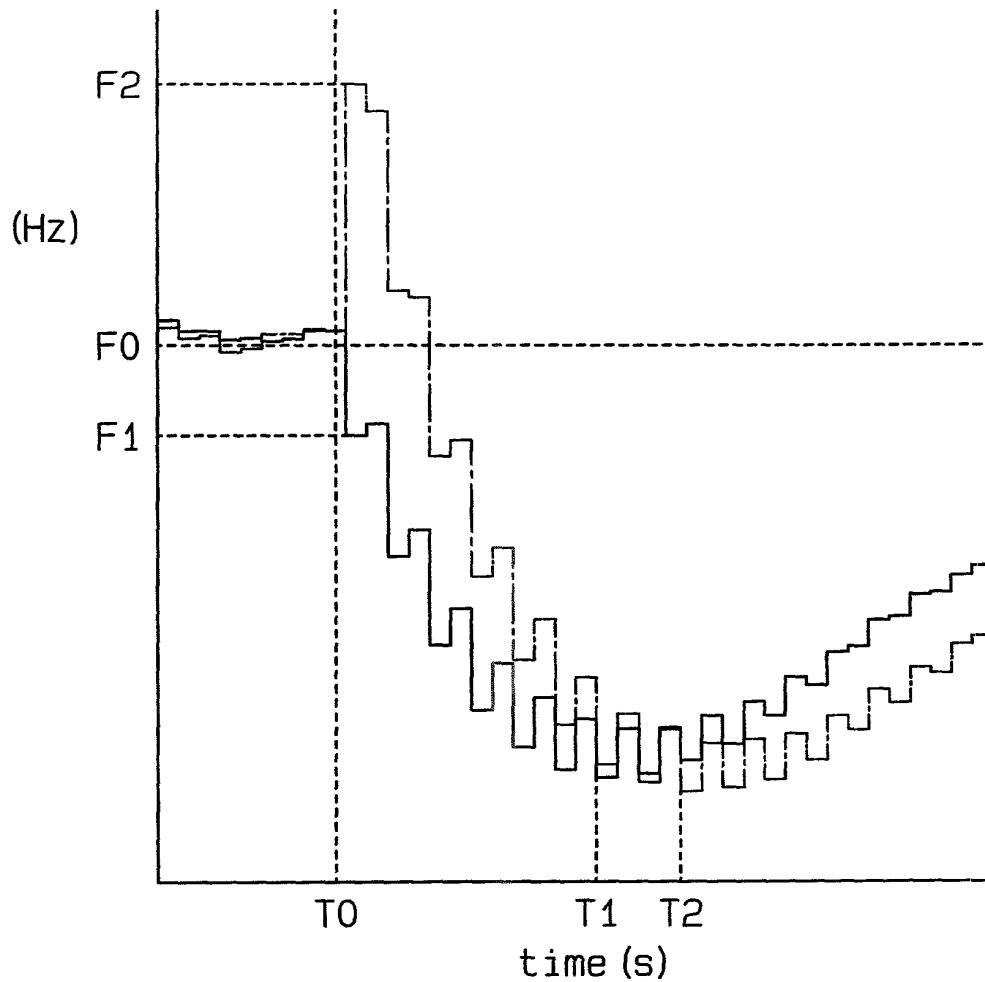


Fig.6



Solid-line Curve : Locked Waveform
According to Present Embodiment

Dot-and-dash-line Curve : Locked
Waveform in Conventional Circuit

Fig. 7

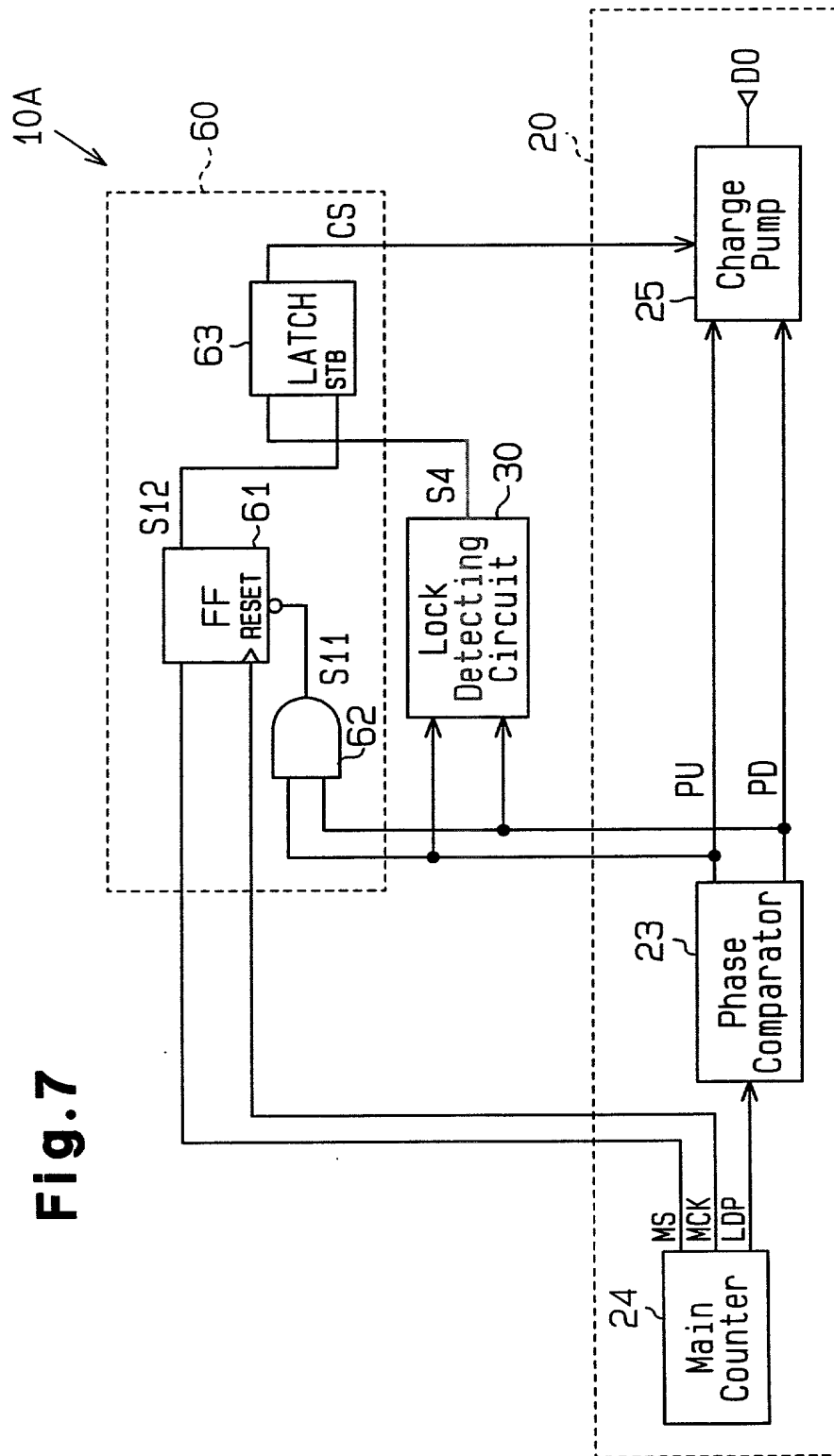


Fig. 8

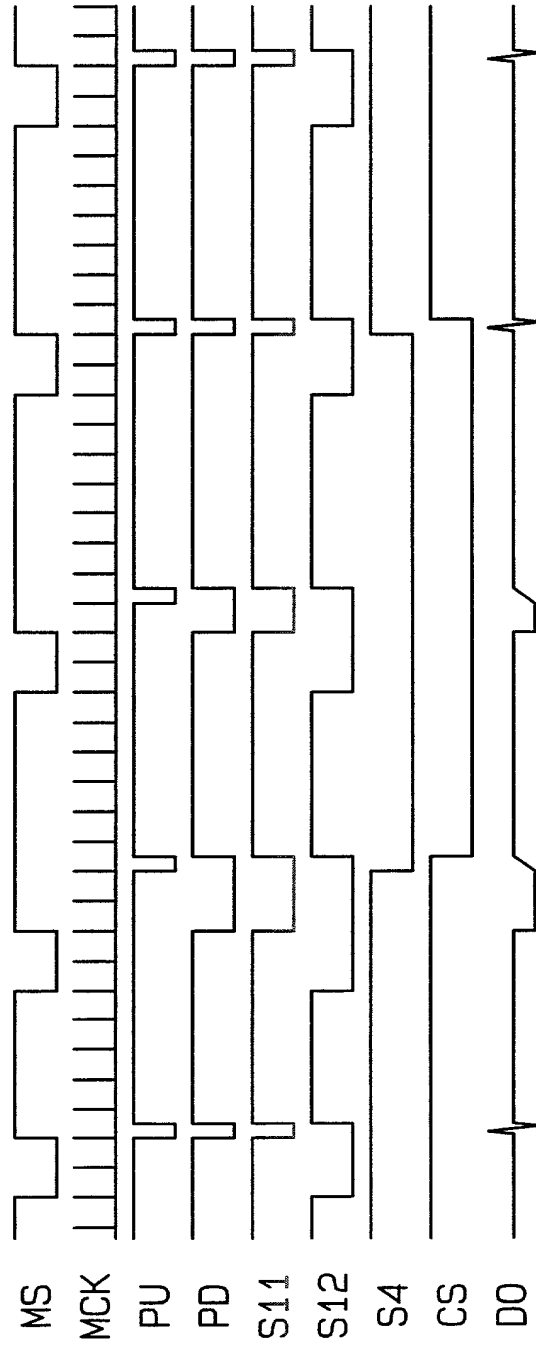


Fig. 9

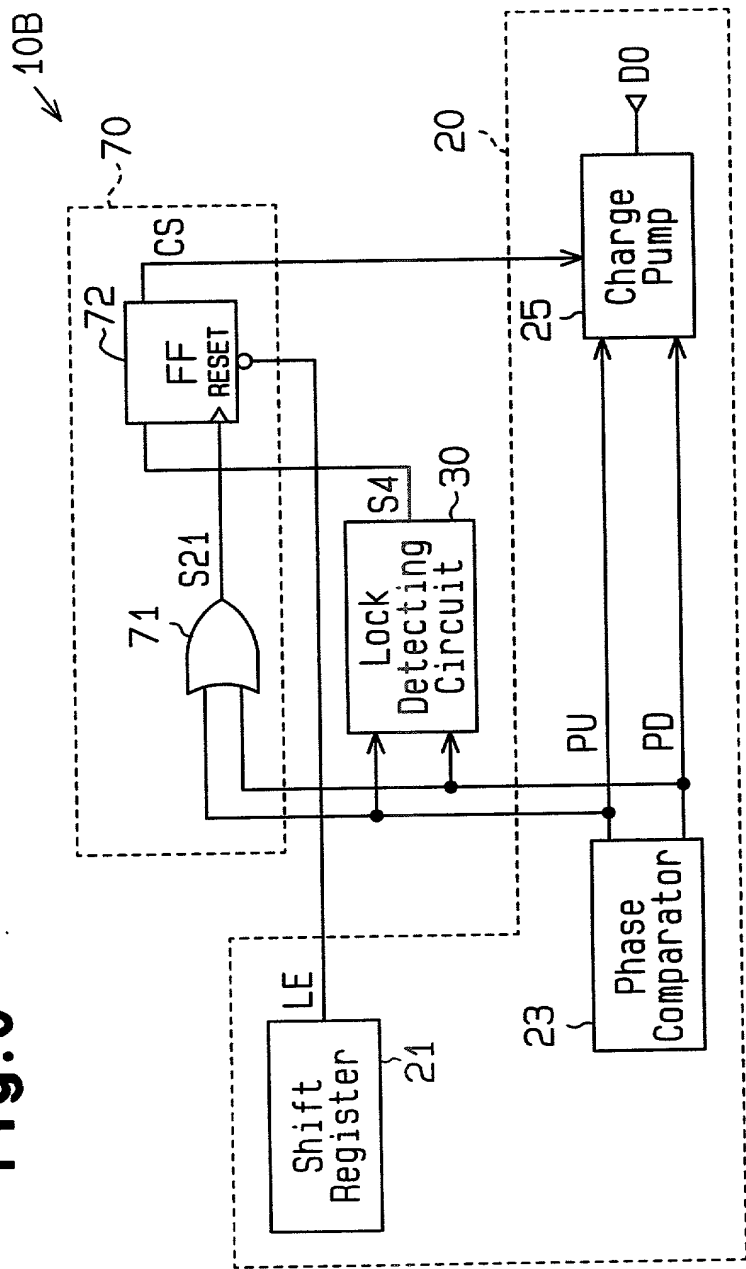


Fig.10

